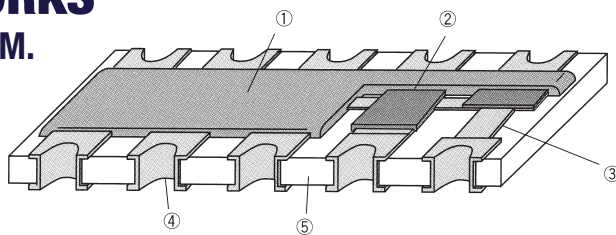
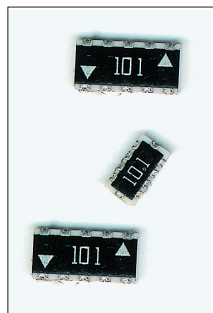


## BUSSED R-NETWORKS CONVEX OR CONCAVE TERM. DUAL COMMON CND



### STRUCTURE

- 1 Protective coating
- 2 Resistive Film
- 3 Inner electrode
- 4 Outer Electrode
- 5 Ceramic substrate



### IDENTIFICATION

TYPE	COATING COLOR	MARKING
CND1J10VK <sup>a</sup> , CND2B10 <sup>b</sup>	Black	White, 3 digits „•“ marks pin 1 & 6
CND1J10K <sup>a</sup>		White, 3 digits „•“ marks pin 5 & 10
CND2B10V <sup>b</sup>		White, 3 digits „▼“ marks pin 1 & 6
CND1J10Y, CND2A10Y		White, 3 digits

<sup>a</sup> Convex types: marking on common pins

<sup>b</sup> Concave types: marking on pin 1 & 6

Products with Pb-free terminations meet EU-RoHS requirements

NETWORKS  
(PASSIVE COMPONENTS)

### TYPE DESIGNATION (HOW TO ORDER)

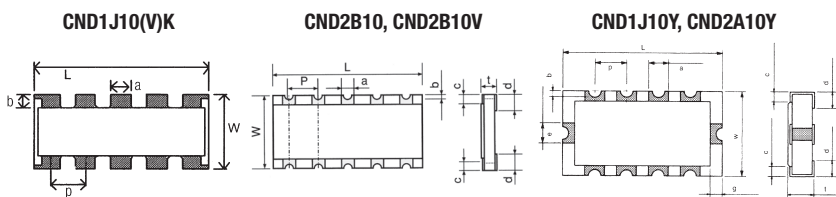
CND	1J	10	V	K	T	TD	103	J	Contact us when you have control request for environmental hazardous material other than the substance specified by EU-RoHS
PRODUCT CODE	STYLE 1J, 2A 2B	NO. OF TERMINALS	CIRCUIT SYMBOL	TERM. STYLE	TERMINATION SURFACE MATERIAL	TAPING* TD, TE, BK	NOMINAL RESISTANCE 3 digits	TOLERANCE J: (±5%)	
			*Blank: Standard common type V: Reserve common type Y: side electrode type	*Blank: concave K: convex	T: Sn (L: Sn/Pb)	*Please see "PACKAGING"			

### DIMENSIONS (mm)

TYPE	L	W	c	d	t	a (top)	a (bottom)	b ±0.1	P (ref.)	e	g
CND1J10(V)K	3.2 ± 0.1	1.6 ± 0.1	0.3 ± 0.2	0.3 ± 0.1	0.5 ± 0.1	0.4 ± 0.1	-	(0.3)	(0.64)	-	-
CND1J10Y	3.2 ± 0.15	1.6 ± 0.15	0.2 ± 0.1	0.35 ± 0.1	0.55 ± 0.1	0.33 ± 0.15	0.3 ± 0.1	(0.1)	(0.635)	0.4 ± 0.1	0.2 ± 0.1
CND2A10Y	4.0 ± 0.2	2.1 ± 0.2	0.25 ± 0.2	0.4 ± 0.2	0.6 ± 0.1	0.5 ± 0.2	0.4 ± 0.15	0.15	(0.8)	0.5 ± 0.2	0.3 ± 0.2
CND2B10 (V)	6.4 ± 0.2	3.1 ± 0.2	0.35 ± 0.15	0.55 ± 0.15	0.6 ± 0.1	0.6 ± 0.1	0.6 ± 0.15	0.15	(1.27)	-	-

### FEATURES

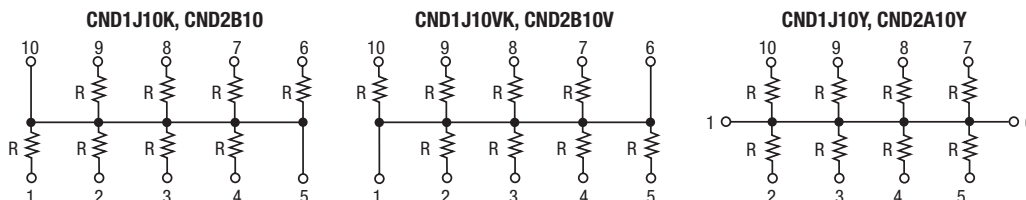
- 8 bussed resistor elements included in one array
- Concave or convex terminations
- Less board space than individual chip resistors
- High cost reduction efficiency by eliminating moulder operations
- High self-alignment effect in reflow soldering process
- Suitable for image recognition systems due to square corner design
- Rated ambient temperature: +70°C
- Operating temperature range: -55°C ... +125°C
- Suitable as pull-up/pull-down resistors for digital circuits
- Meets or exceeds IEC 60115-1 and JIS C 5201-1
- Suitable for reflow soldering



### RATING

SIZE	TYPE	T.C.R. (ppm/K)	POWER RATING (per element)*	MAX. WORKING VOLTAGE	MAX. OVERLOAD VOLTAGE (5 sec. max.)	RESISTANCE RANGE (E12)	RESISTANCE TOLERANCE
1206	CND1J10(V)K	± 200	0.031 W	25 V	50 V	47 Ω ... 39 kΩ	J (± 5%)
1206	CND1J10Y		0.05 W			22 Ω ... 39 kΩ	
1608	CND2A10Y		0.063 W	100 Ω ... 100 kΩ			
2512	CND2B10(V)		50 V	100 Ω ... 100 kΩ			

### CIRCUIT CONSTRUCTION



\* For resistors operated at an ambient temperature of +70°C or above, the power rating shall be derated. Rated voltage = √(Power rating x resistance value or max. working voltage, whichever is lower).

Please note that network resistors generate higher heat rather than single flat chip resistors even under rated power output. A few cross talks will happen in network resistors.

Design the circuit taking the effect by the cross talks into consideration as very low voltage will occur to the resistor elements that don't pass current by the voltage drop in common electrode if current flows to the common electrodes.

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